

Docket No.: GR 98 P 1507 P



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Date: November 21, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andreas Rusch et al.
Applic. No. : 09/685,361
Filed : October 10, 2000
Title : Semiconductor Memory Device and Method for Fabricating It
Examiner : Lynne Ann Gurley - Art Unit: 2812
Date of Notice of Allowance: August 21, 2002

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,170,227 (Kaneko et al.), dated December 8, 1992;

United States Patent No. 5,563,098 (Kuo et al.), dated October 8, 1996;

United States Patent No. 5,620,926 (Itoh), dated April 15, 1997.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to

03/11/2003 the applicant 121099 09685361
01-FC-1460 130.00 CH

Adjustment date: 00/00/0000 06/22/03
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It is believed that the enclosed prior art is less pertinent than the prior art previously submitted and cited by the Examiner. Kindly place the references in the Patent Office file wrapper.

Respectfully submitted,



For Applicants

Date: November 21, 2002

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